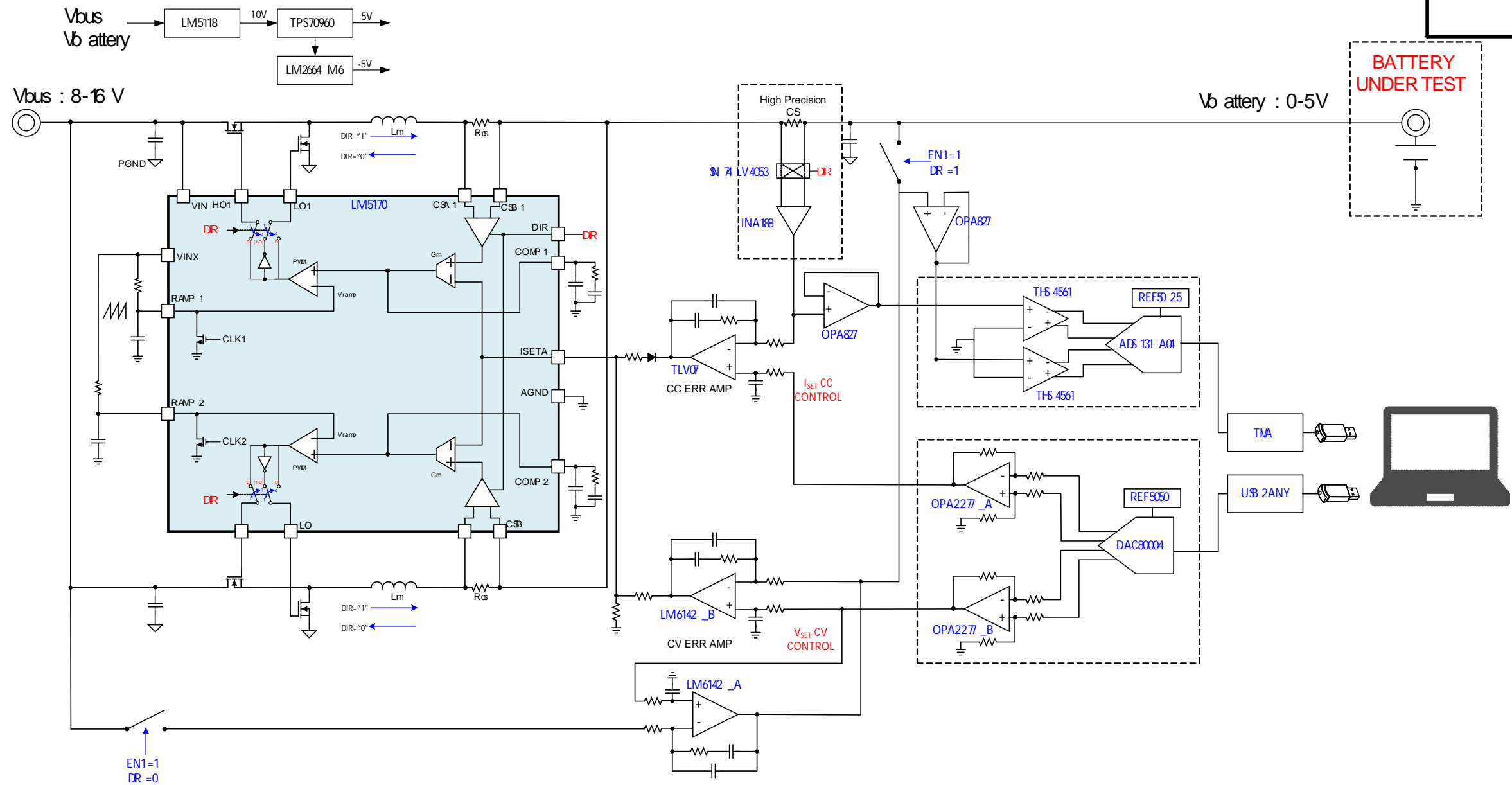
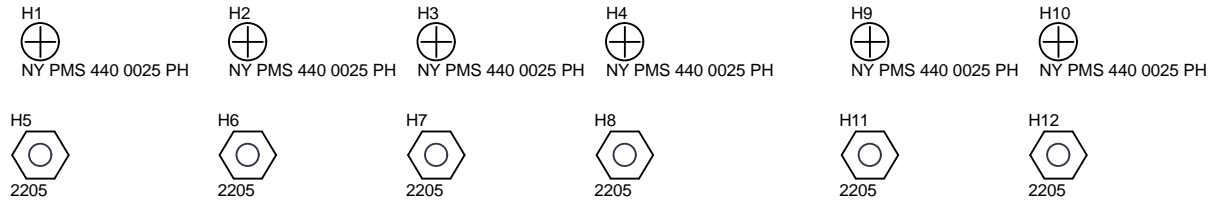


Revision History	
Revision	Notes

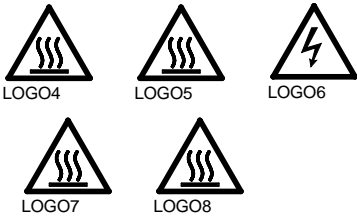




PCB Number: TIDA-01041  
PCB Rev: A

Logo2  
PCB  
LOGO  
Pb-Free Symbol

PCB  
LOGO  
FCC disclaimer



LBL1  
PCB Label

ZZ1  
Label Assembly Note  
This Assembly Note is for PCB labels only

ZZ2  
Assembly Note  
These assemblies are ESD sensitive, ESD precautions shall be observed.

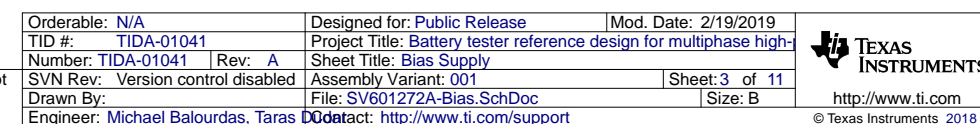
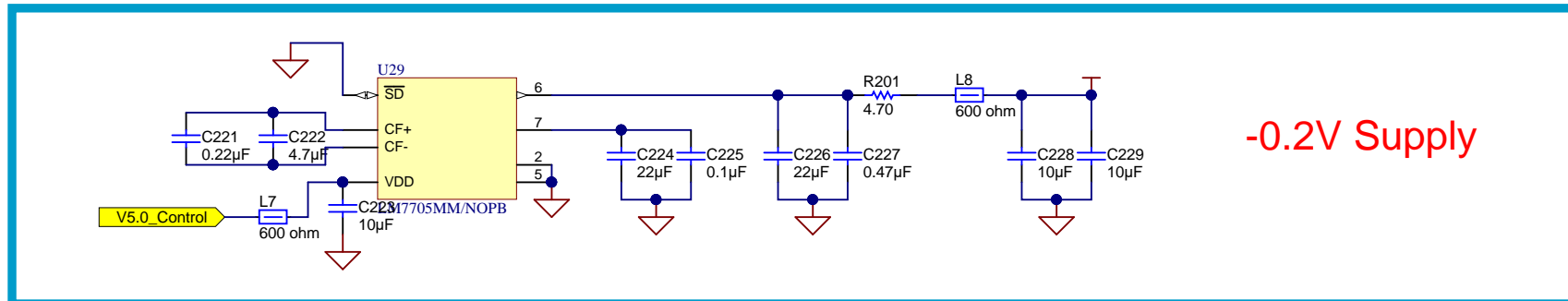
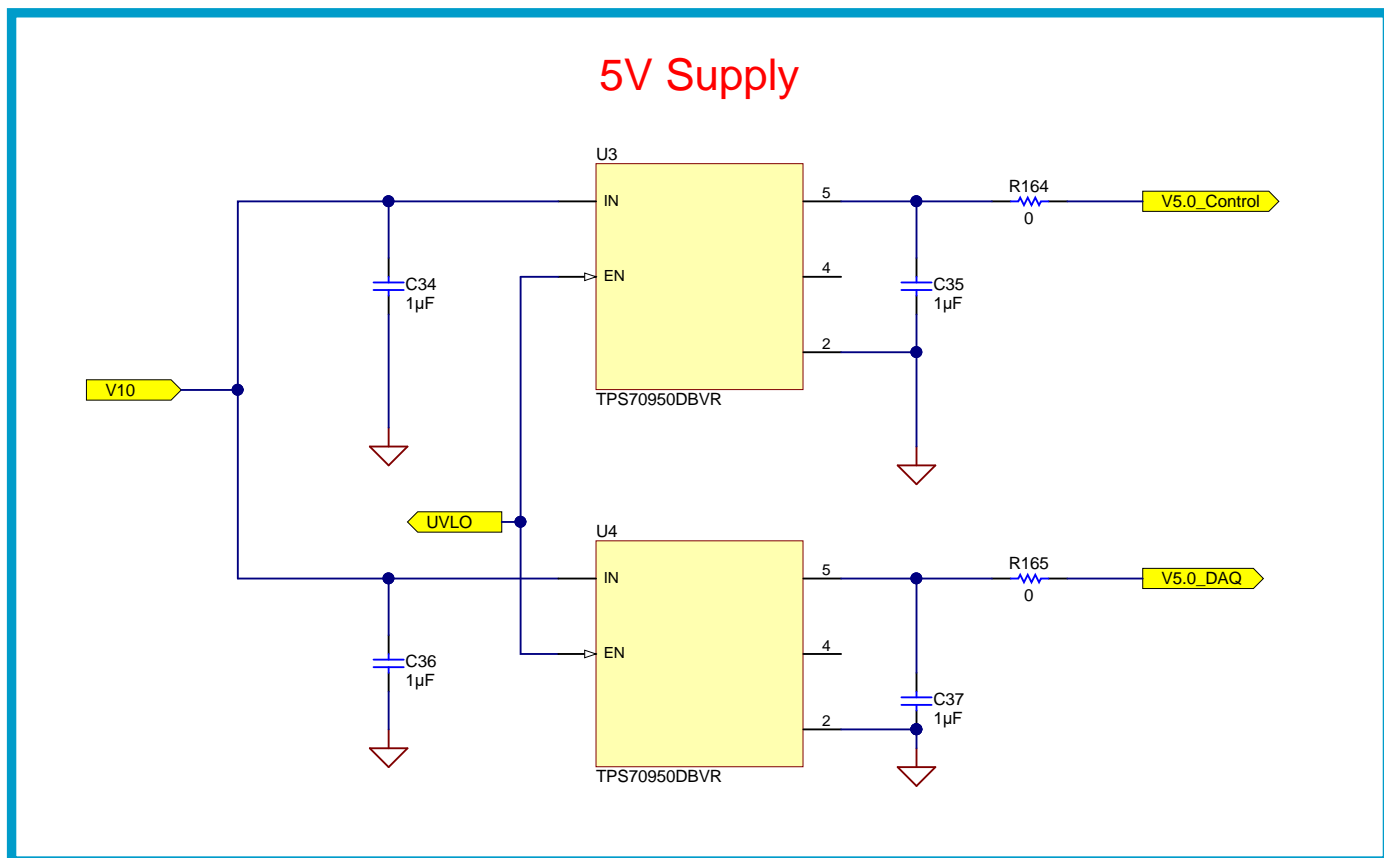
ZZ3  
Assembly Note  
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4  
Assembly Note  
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

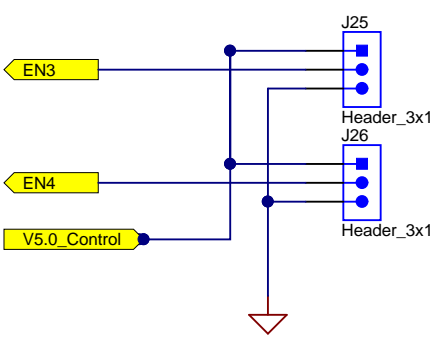
Label Table	
Variant	Label Text
001	
002	

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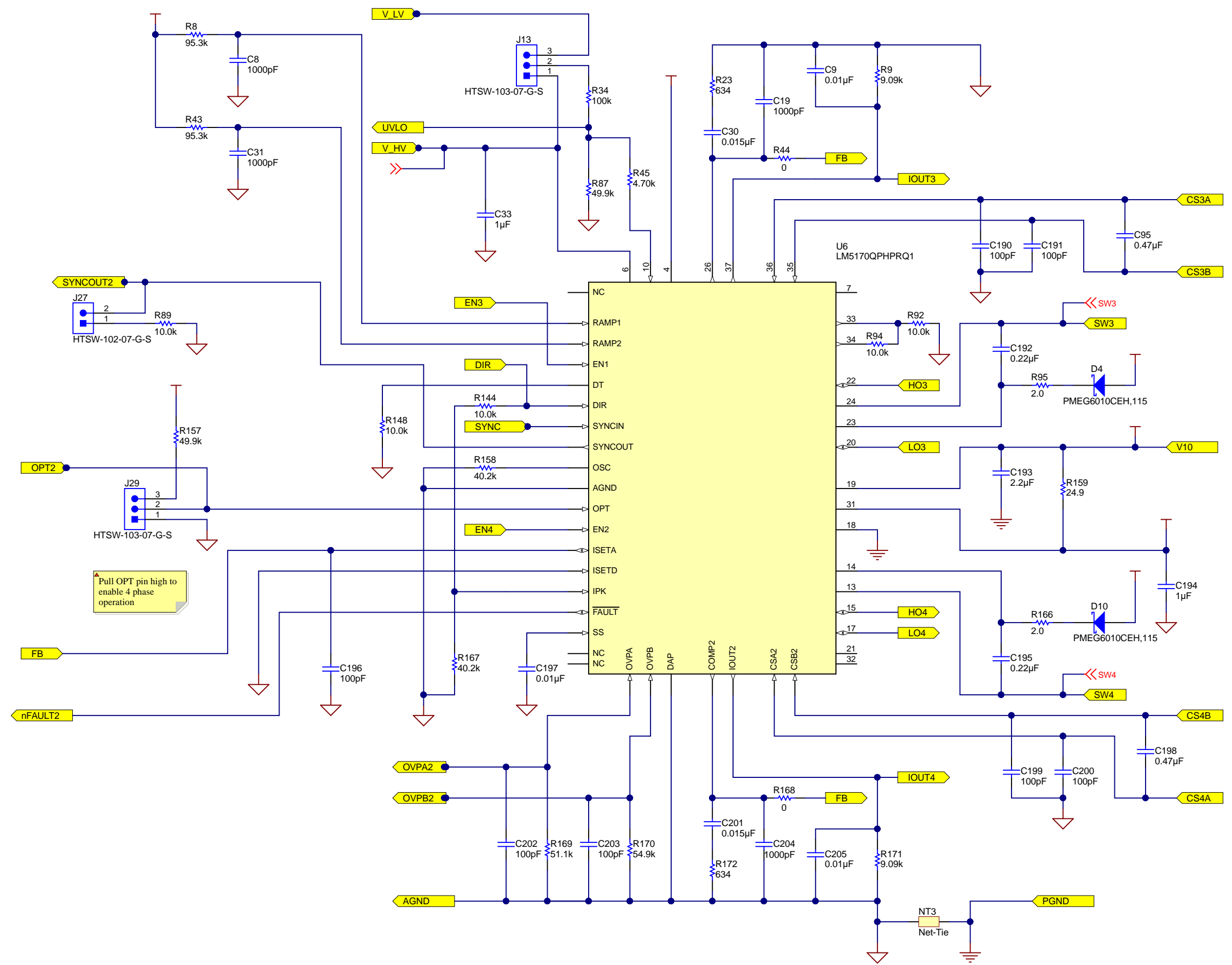
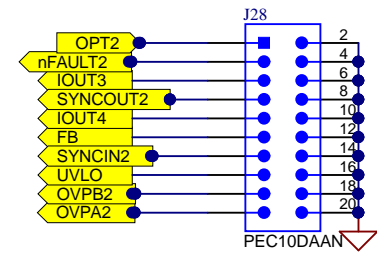
Orderable: N/A	Designed for: Public Release	Mod. Date: 2/19/2019
TID #: TIDA-01041	Project Title: Battery tester reference design for multiphase high-	
Number: TIDA-01041	Rev: A	Sheet Title: Hardware
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 2 of 11
Drawn By:	File: SV601272A-HW.SchDoc	Size: B
Engineer: Michael Balourdass, Taras D	Contact: http://www.ti.com/support	



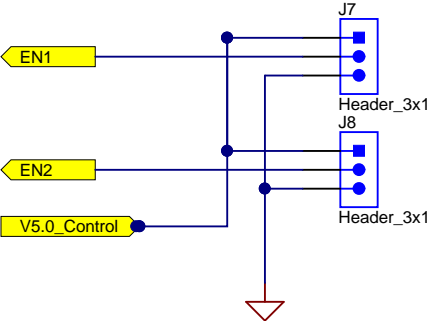
# Enable Jumpers



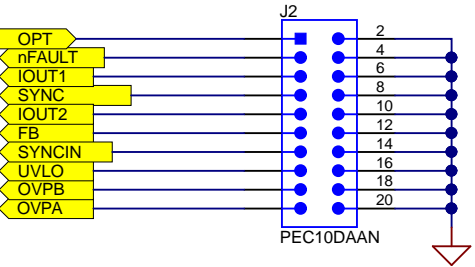
# Optional Ground Jumpers



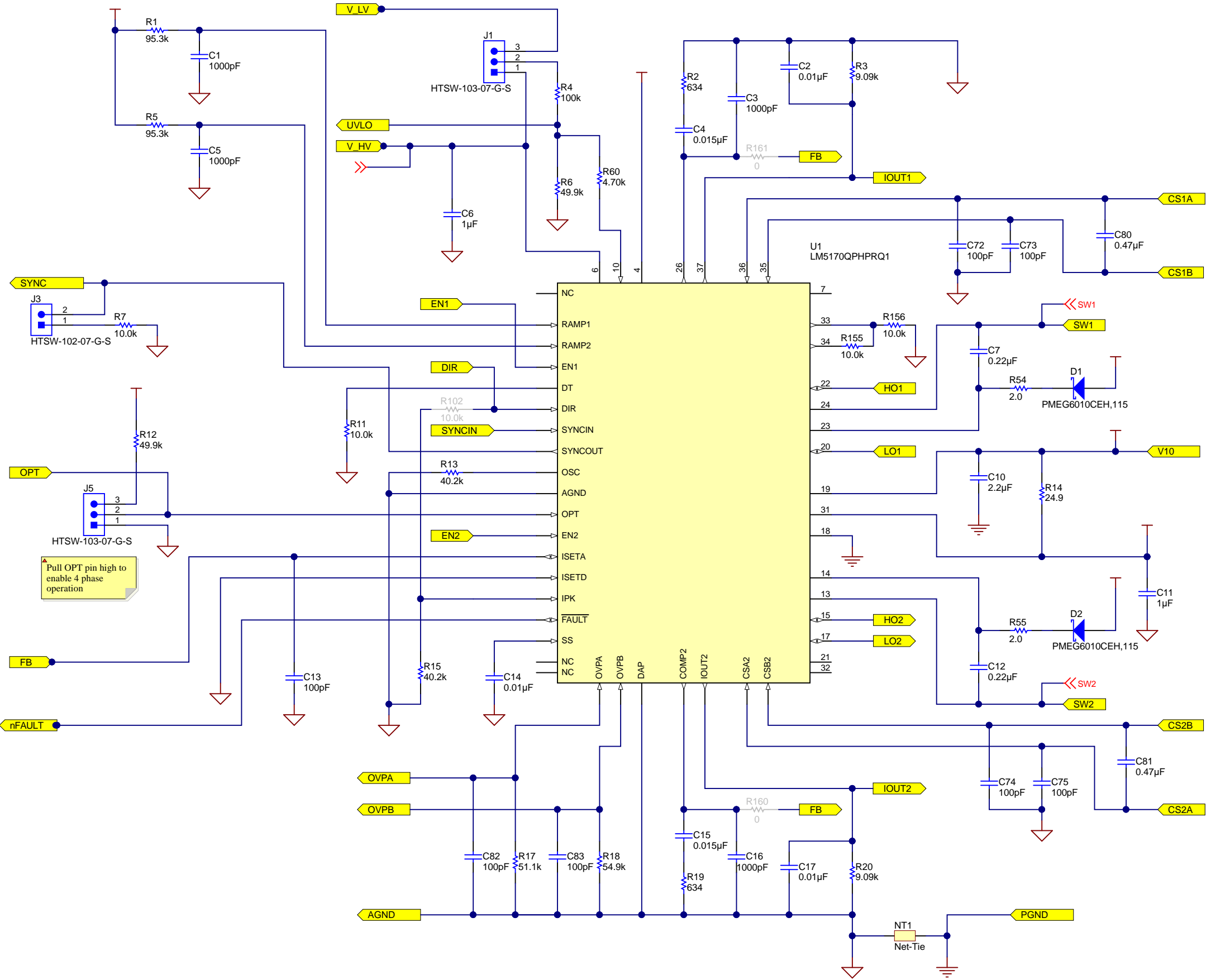
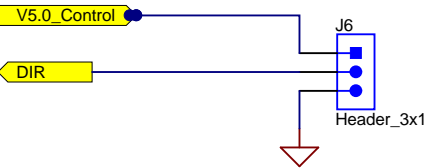
Enable Jumpers



Optional Ground Jumpers

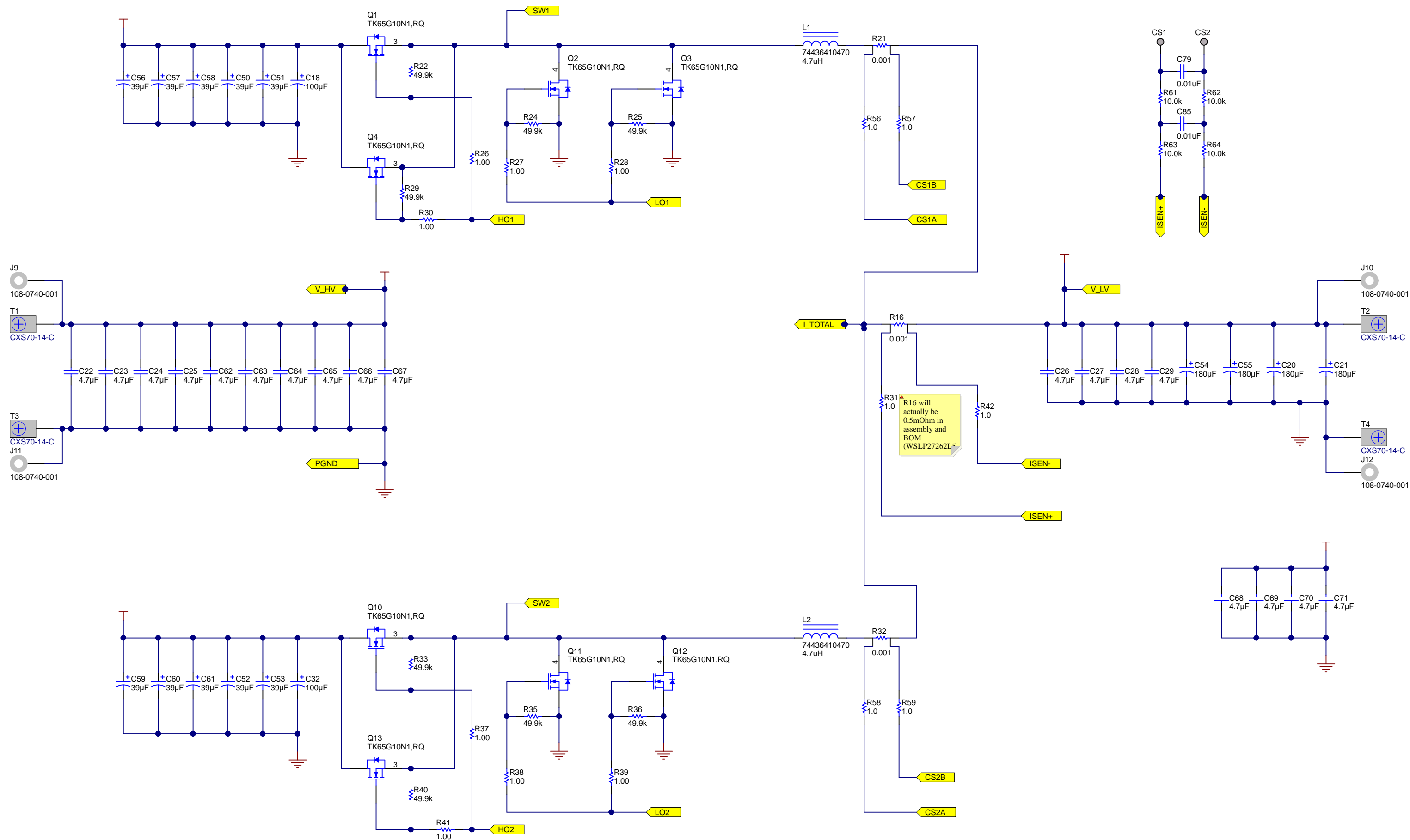


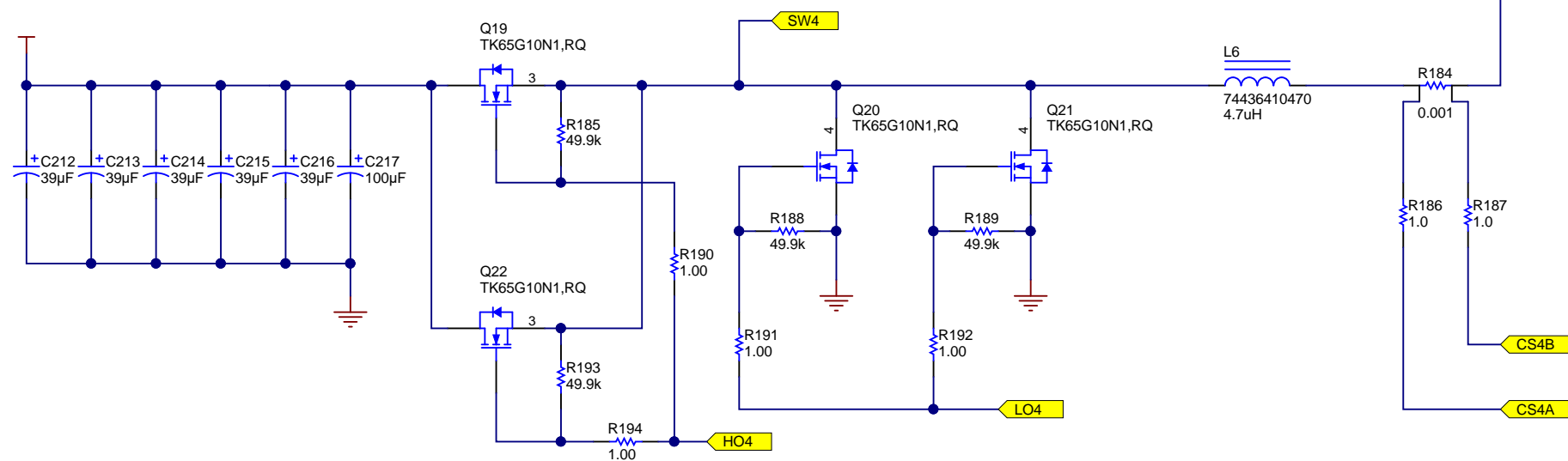
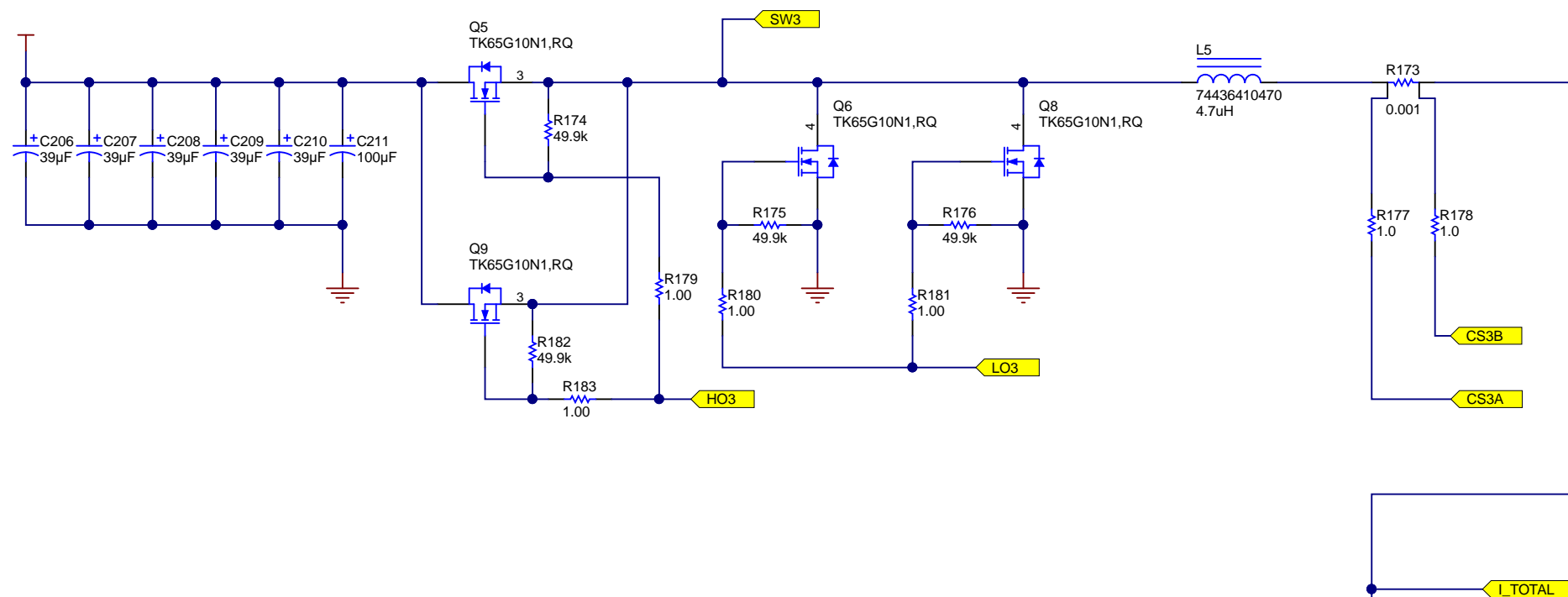
Direction Control



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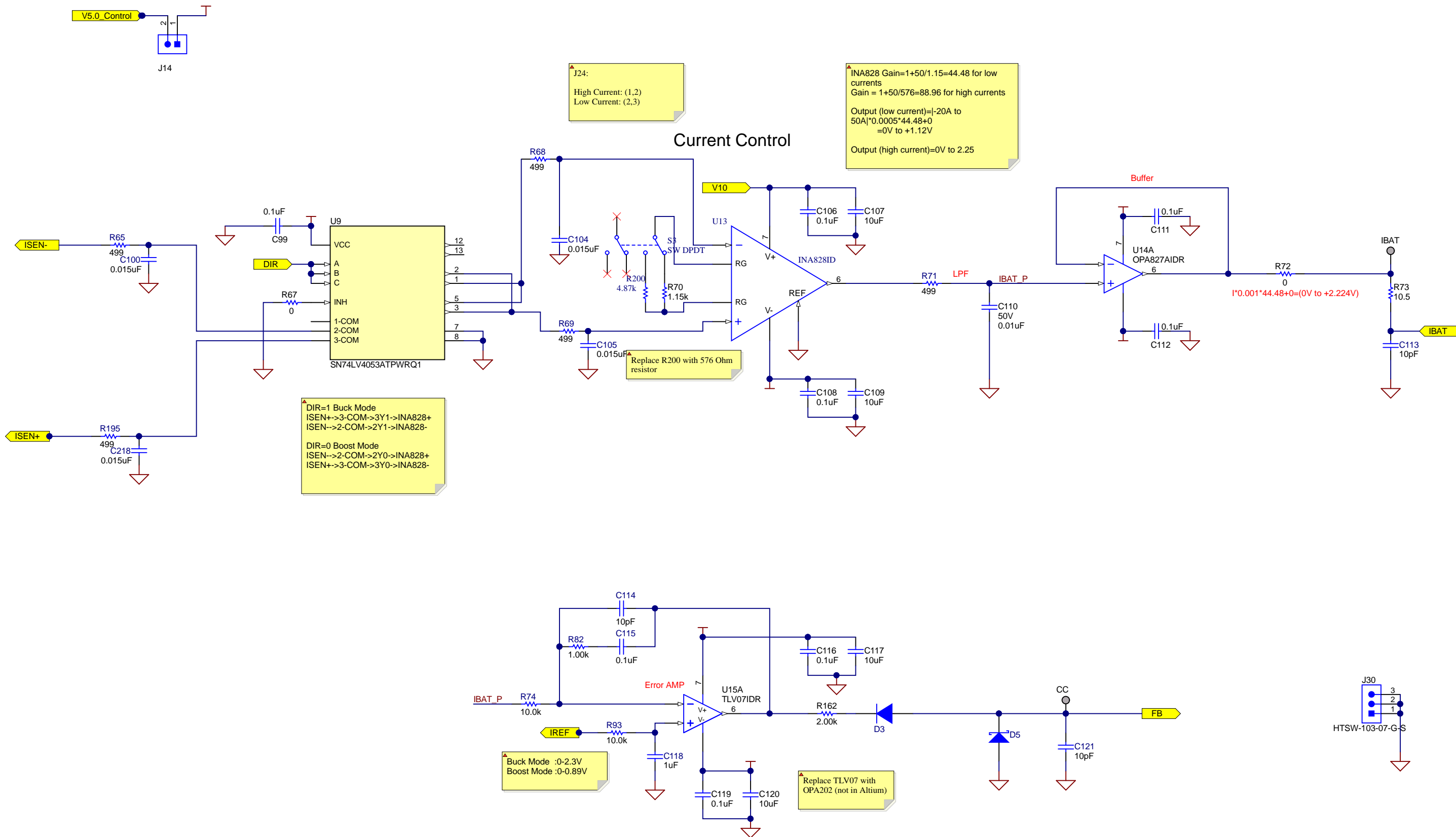
Orderable: N/A	Designed for: Public Release	Mod. Date: 2/19/2019
TID #: TIDA-01041	Project Title: Battery tester reference design for multiphase high-	
Number: TIDA-01041	Rev: A	Sheet Title: Controller
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 4 of 11
Drawn By:	File: SV601272A-Controller1.SchDoc	Size: B
Engineer: Michael Balourdas, Taras D	Docuact: http://www.ti.com/support	





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Orderable: N/A	Designed for: Public Release	Mod. Date: 2/19/2019
TID #: TIDA-01041	Project Title: Battery tester reference design for multiphase high-	
Number: TIDA-01041	Rev: A	Sheet Title: Power Stage
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 5 of 11
Drawn By:	File: SV601272A-Power2 REV2.SchDoc	Size: B
Engineer: Michael Balourdass, Taras D	Docuact: http://www.ti.com/support	



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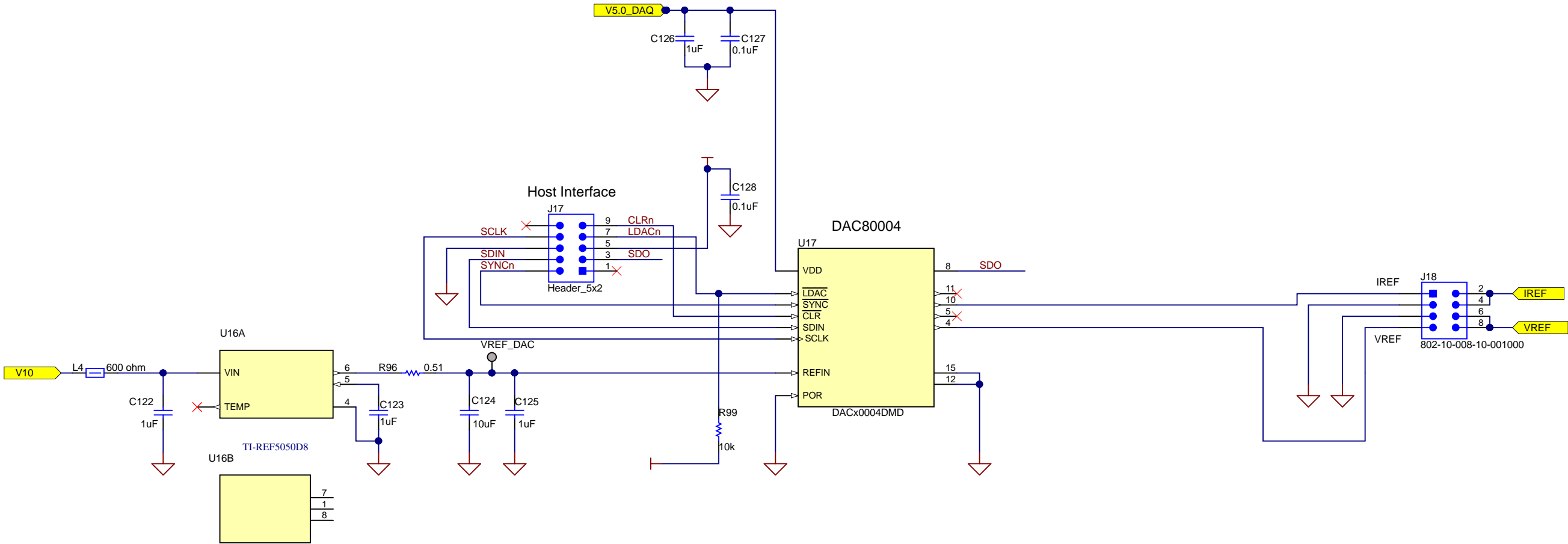




DAC

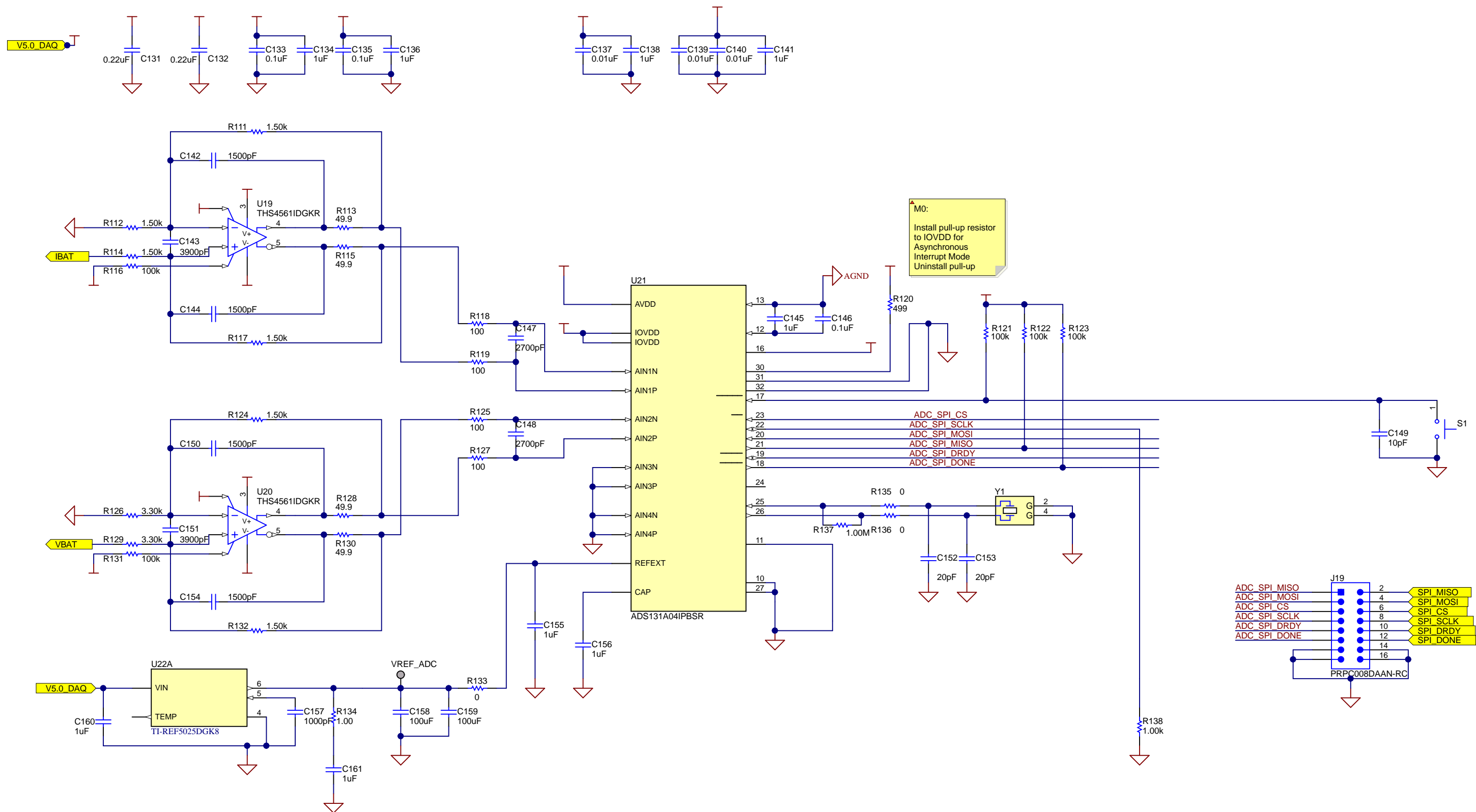
▲ IREF  
Output Range:0V-2.3V

VREF  
Output Range:0V-4.1V



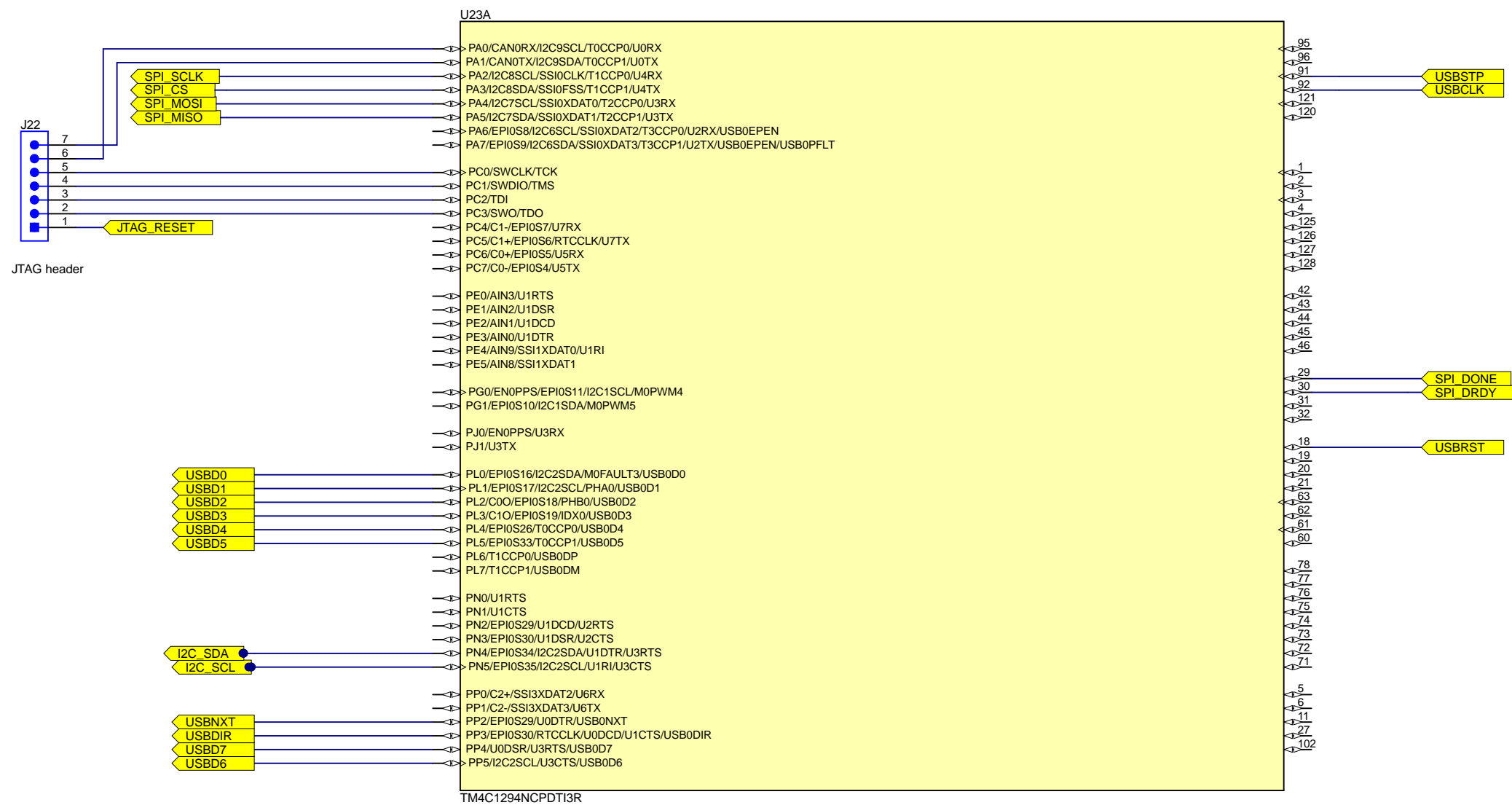
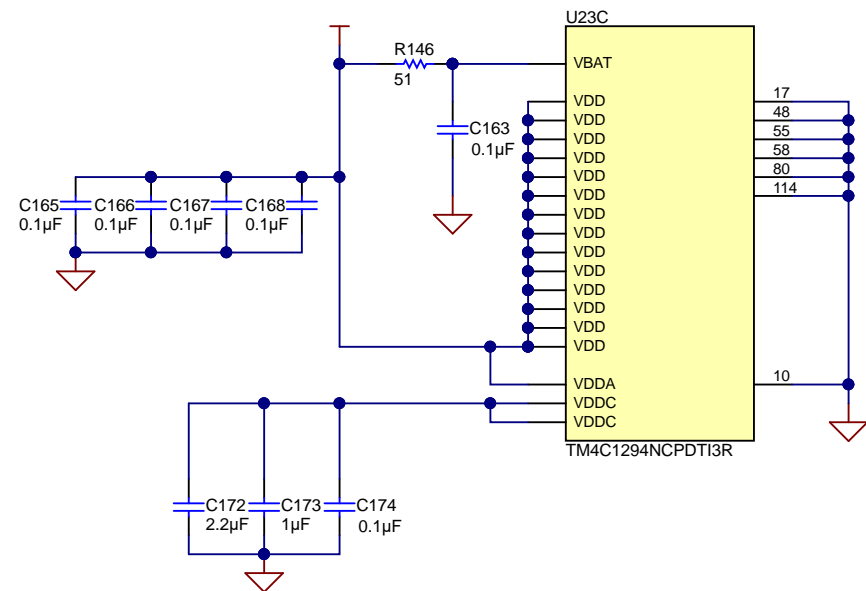
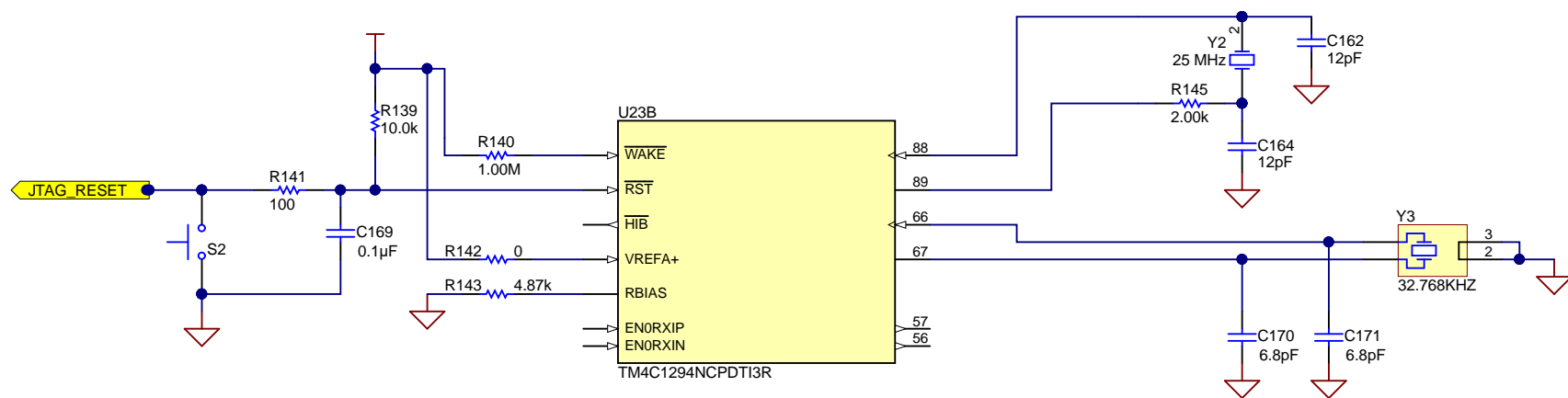
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TID #: TIDA-01041	Project Title: Battery tester reference design for multiphase high-	
Number: TIDA-01041	Rev: A	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 8 of 11
Drawn By:	File: SV601272A-DAC.SchDoc	Size: B
Engineer: Michael Balourdas, Taras D	Contact: http://www.ti.com/support	



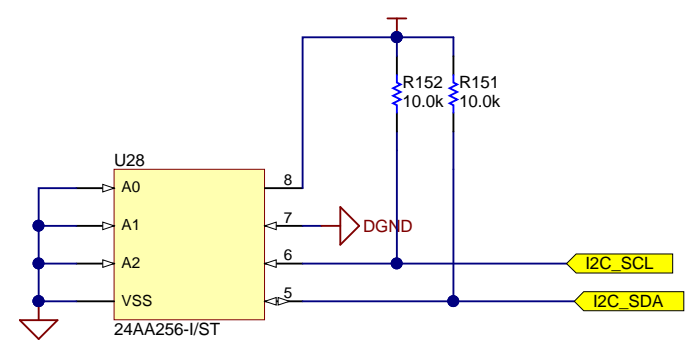
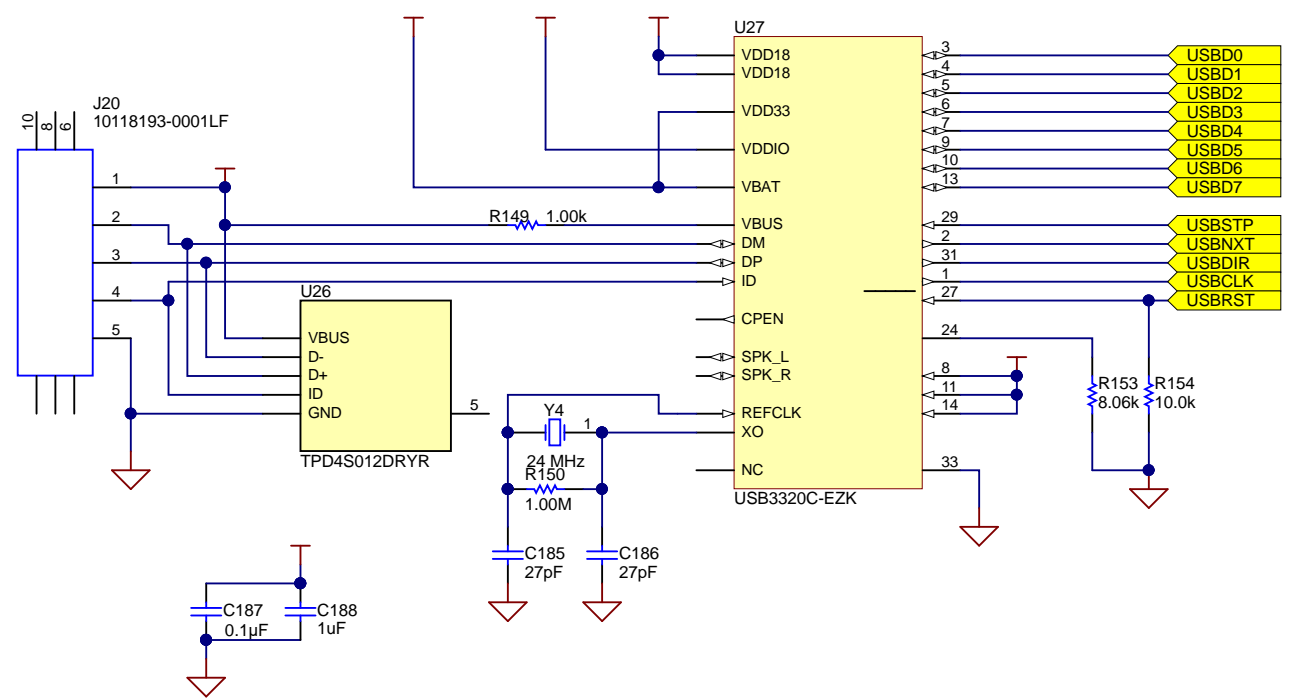
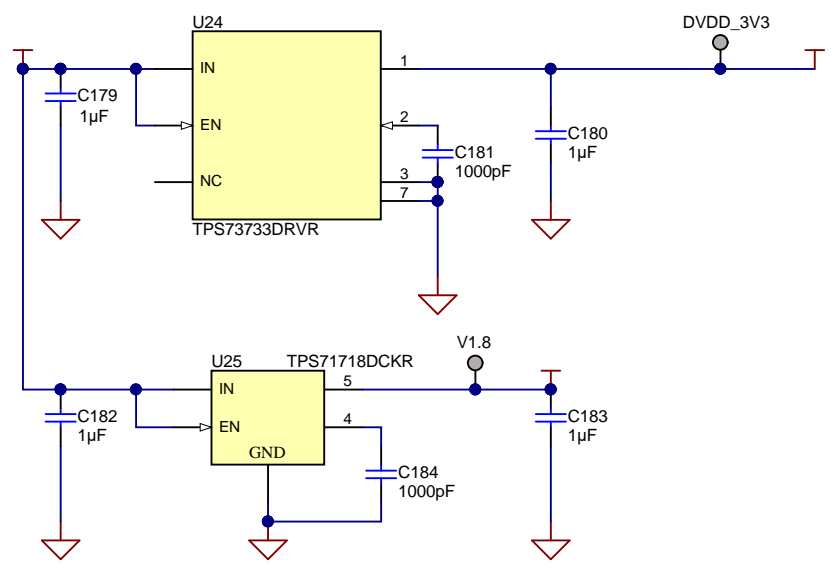
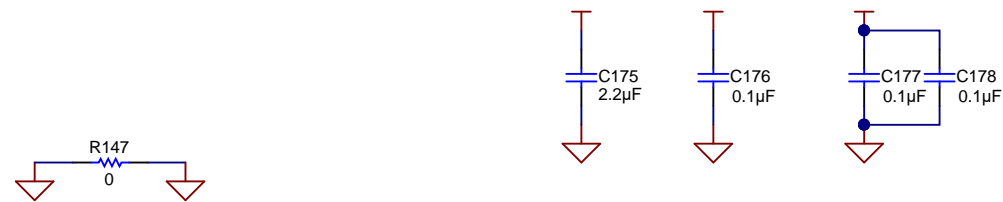
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TID #: TIDA-01041	Project Title: Battery tester reference design for multiphase high-	
Number: TIDA-01041	Rev: A	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 9 of 11
Drawn By:	File: SV601272A-ADC.SchDoc	Size: B
Engineer: Michael Balourd, Taras D	Docuact: http://www.ti.com/support	



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TID #: TIDA-01041	Project Title: Battery tester reference design for multiphase high-	
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SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 10 of 11
Drawn By:	File: SV601272A-TM4C_Main.SchDoc	Size: B
Engineer: Michael Balourdas, Taras D	Docuact: http://www.ti.com/support	



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